

09632867.080400

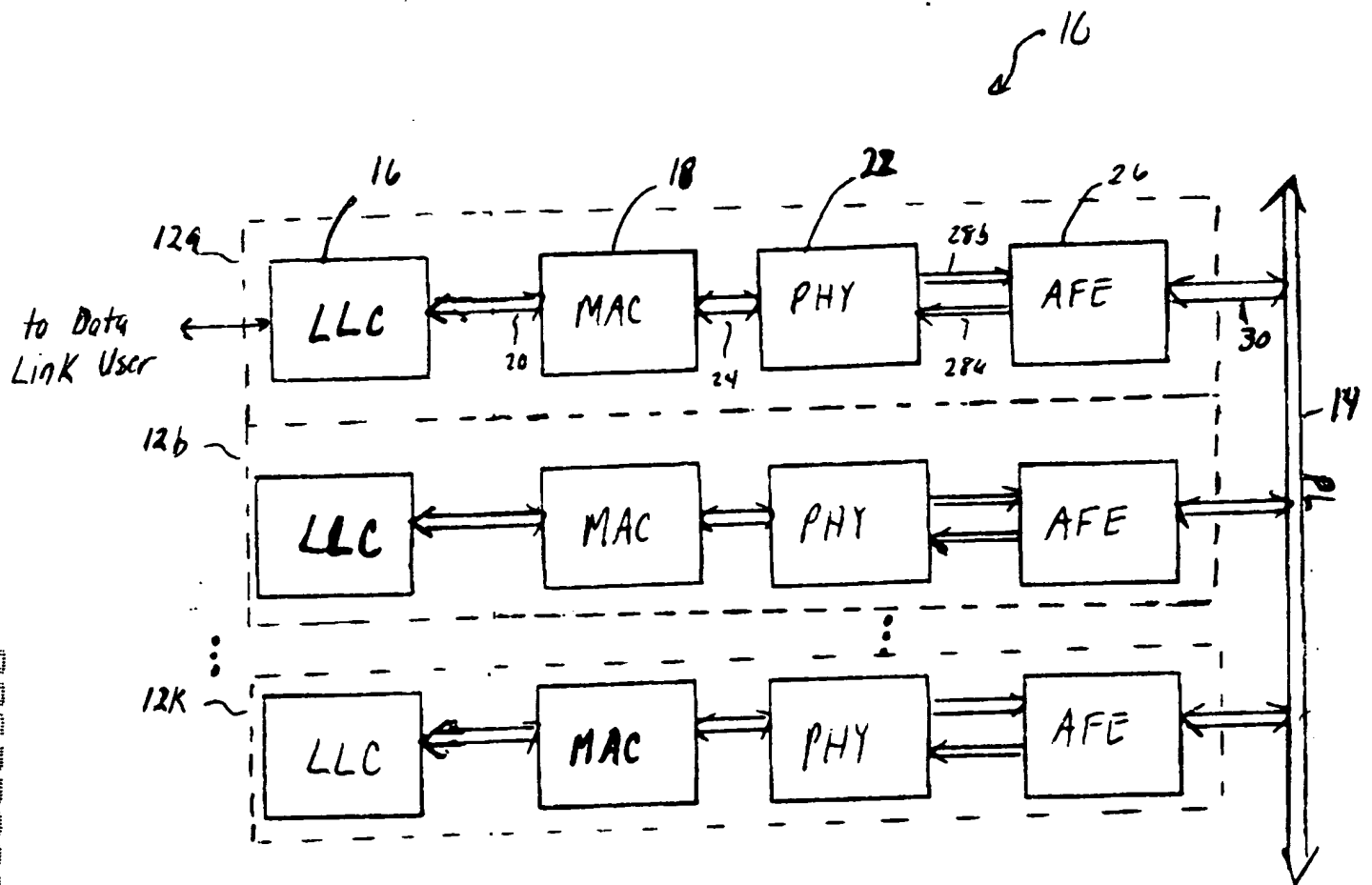


FIG. 1

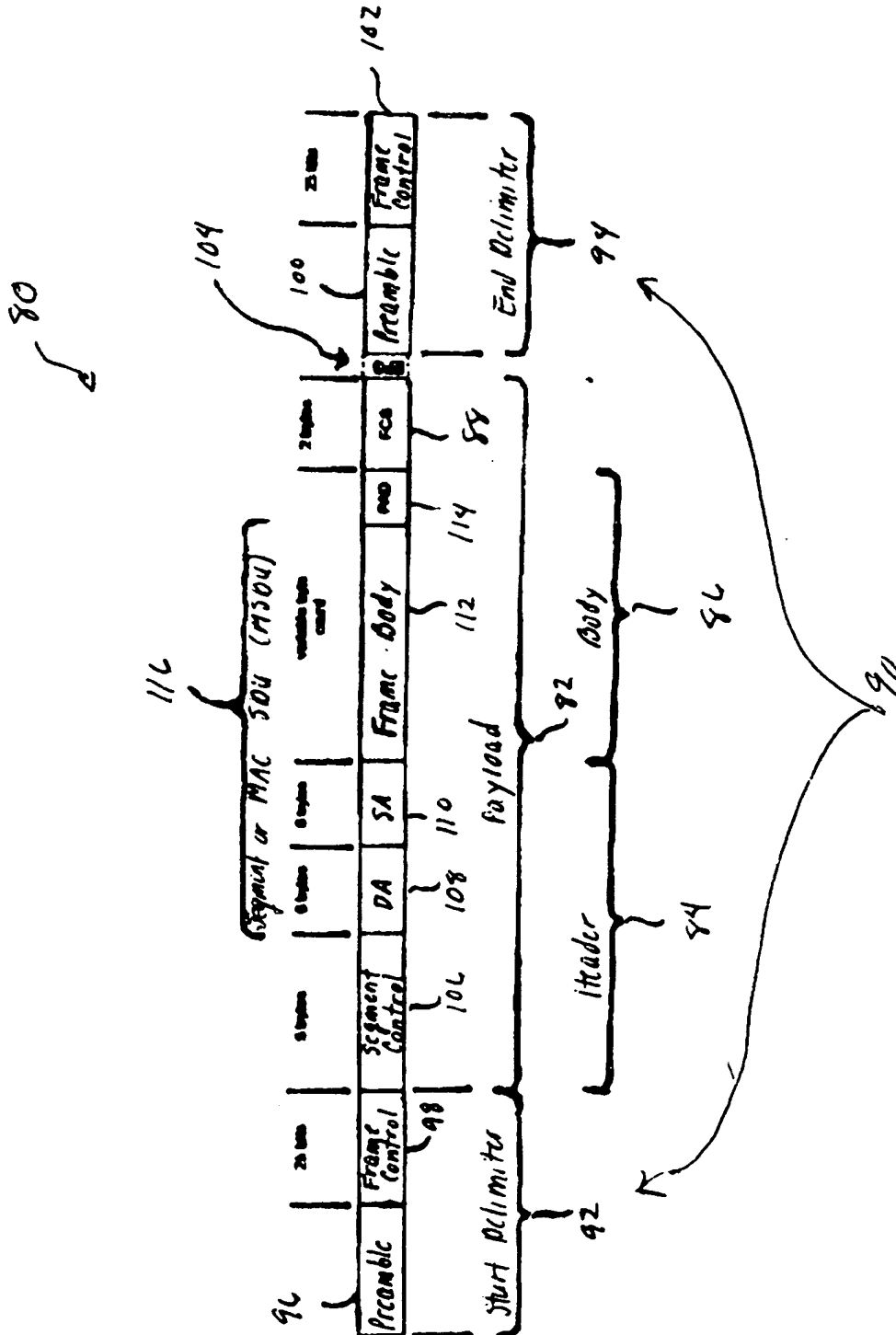


FIG. 3

004080" 2982E960

✓ 120

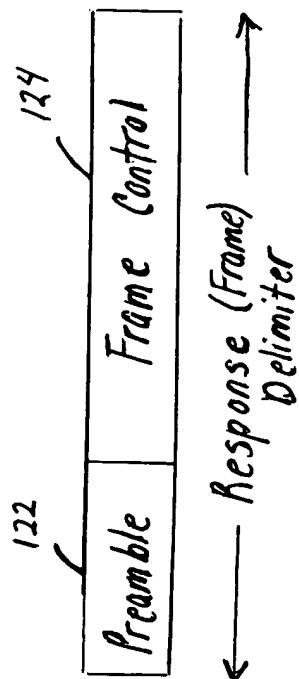


FIG. 4

98

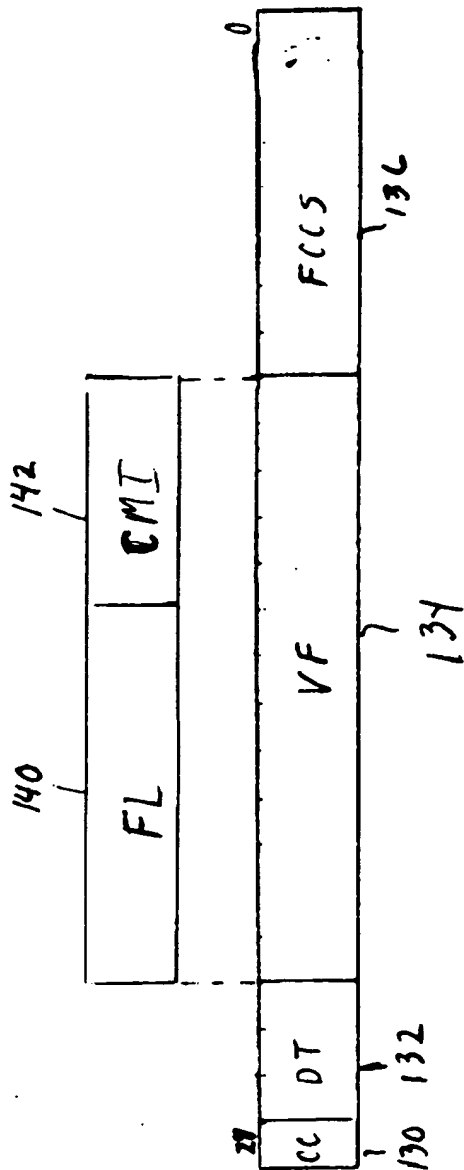


FIG. 5A

102

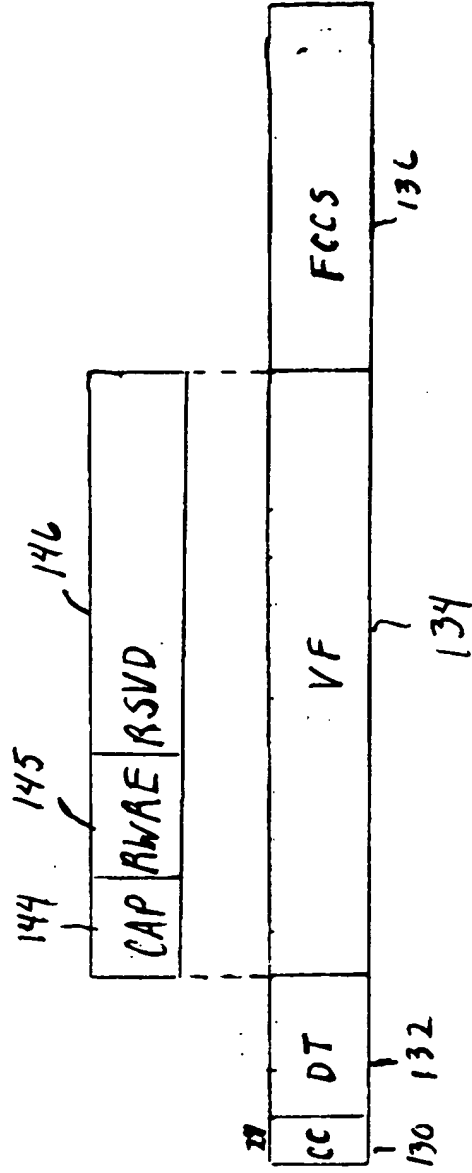


FIG 5B

004080" 2982E960

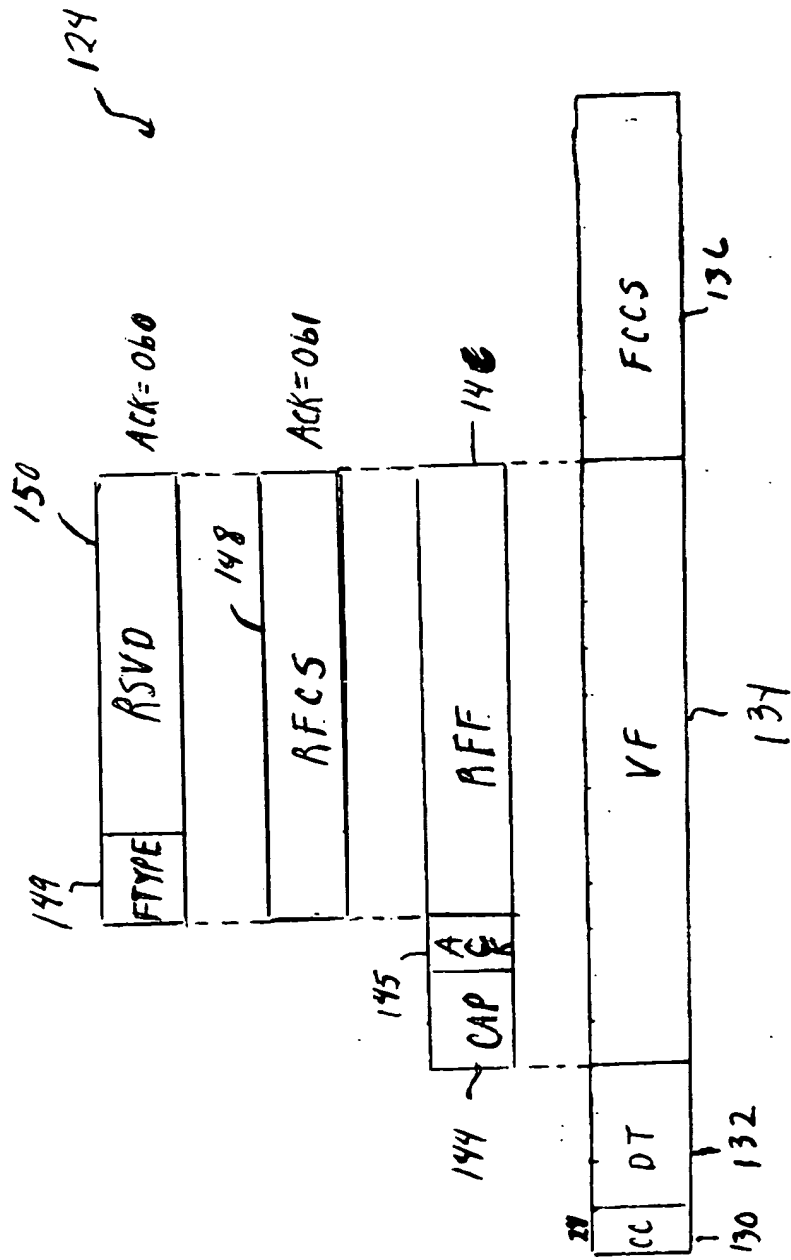


FIG. 6

004080" 2982E960

106 ↗

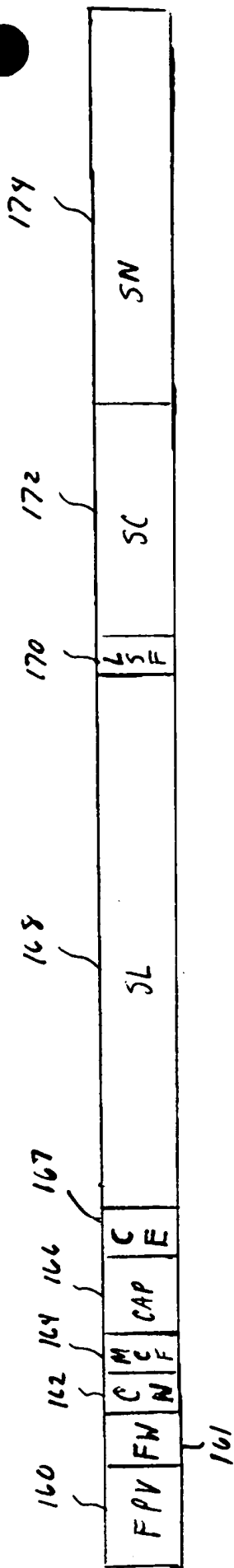


FIG. 7

112 ↙

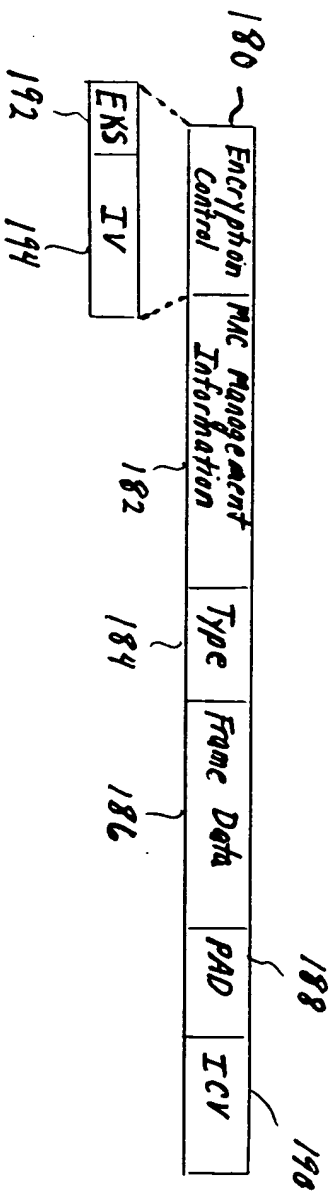


FIG. 8

004080-2982E960 09632867-080400

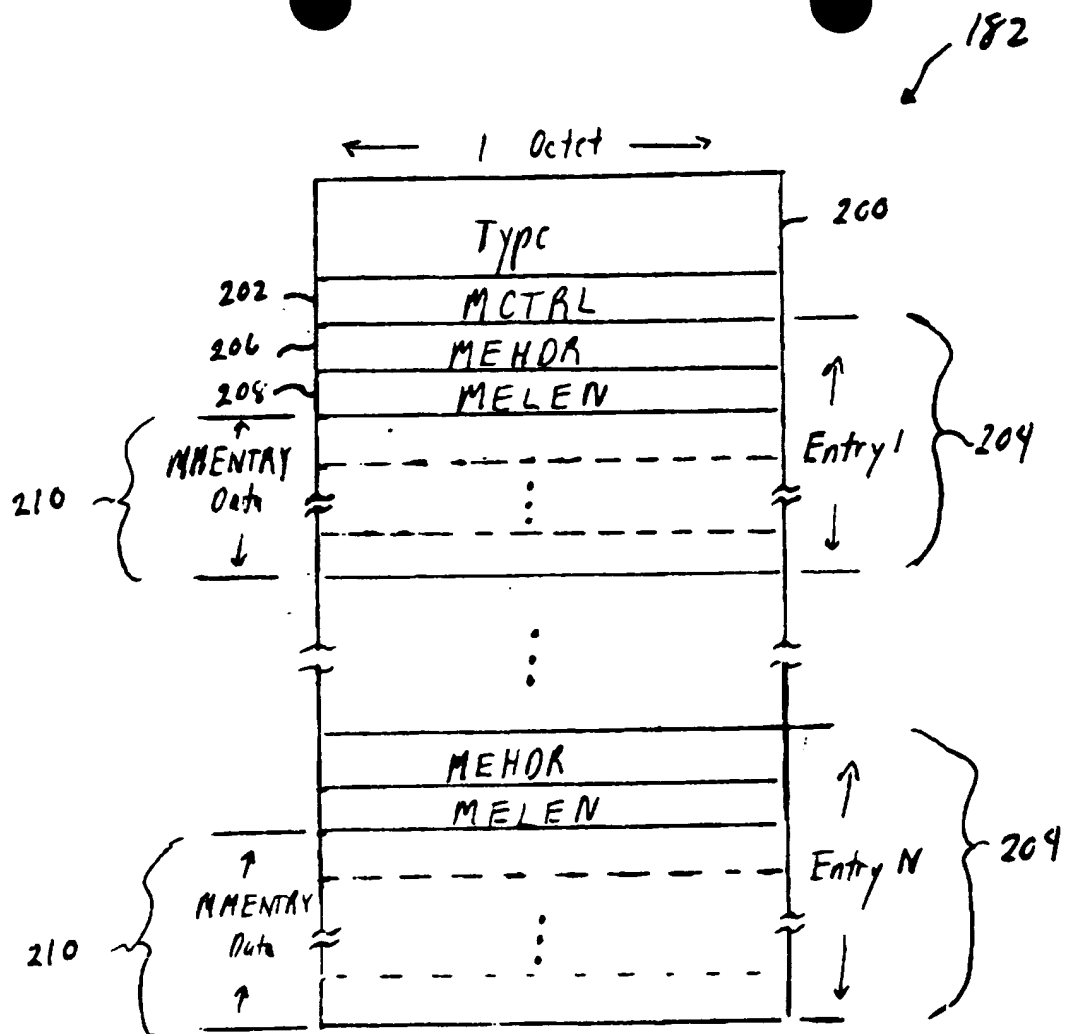


FIG. 1

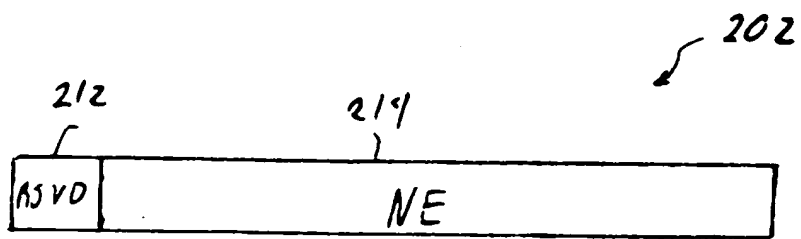


FIG. 10



FIG. 11

004080.2982E960

↙ 210A

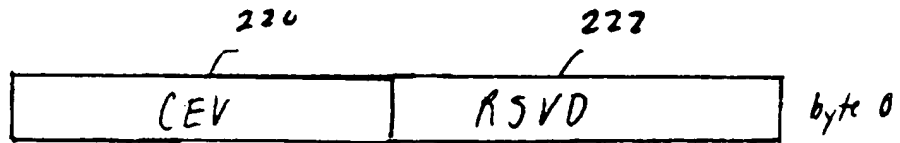


FIG. 12A

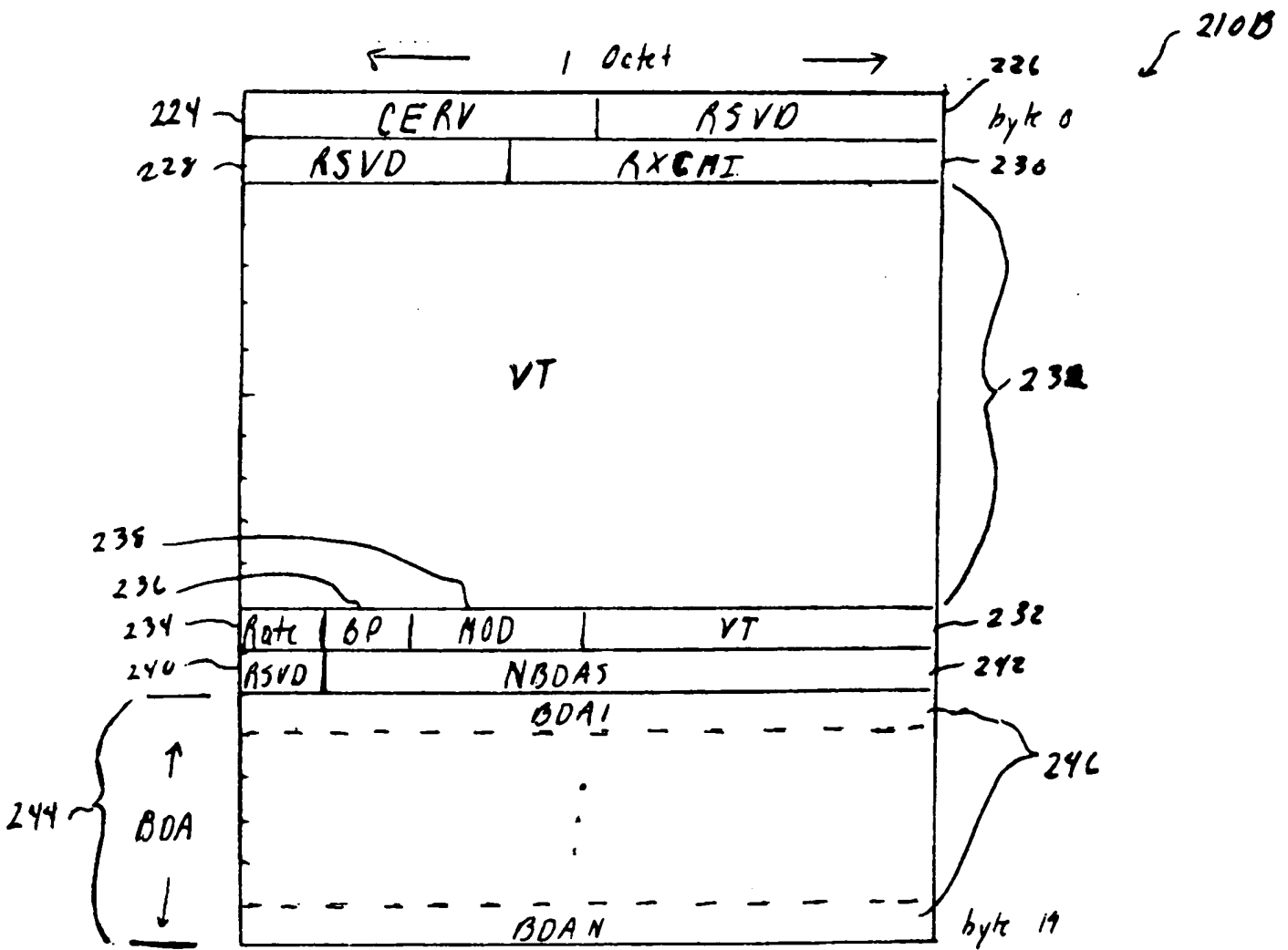


FIG. 12B

004080 7982E660

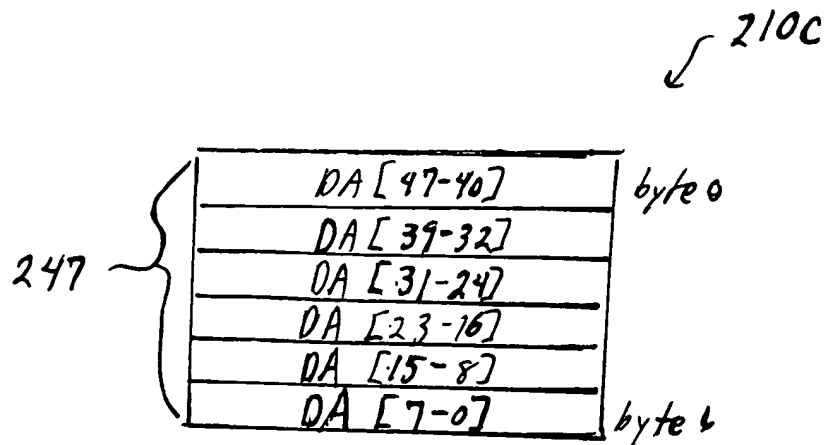


FIG. 13A

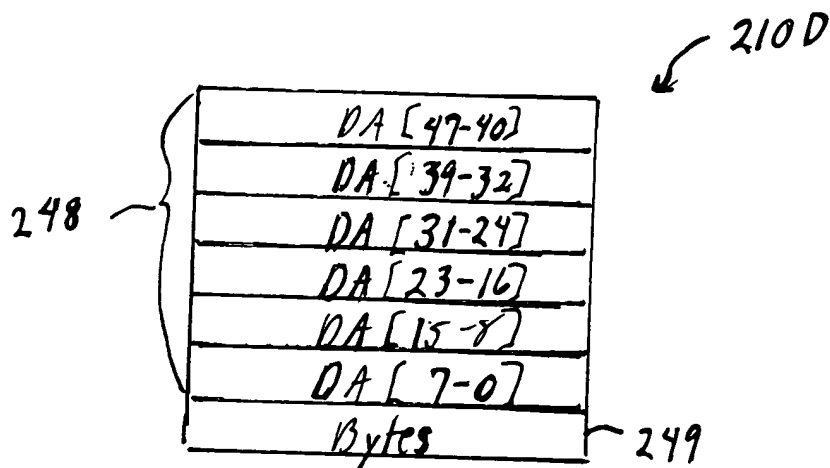


FIG. 13B

0040B0" 2982E960

210 E

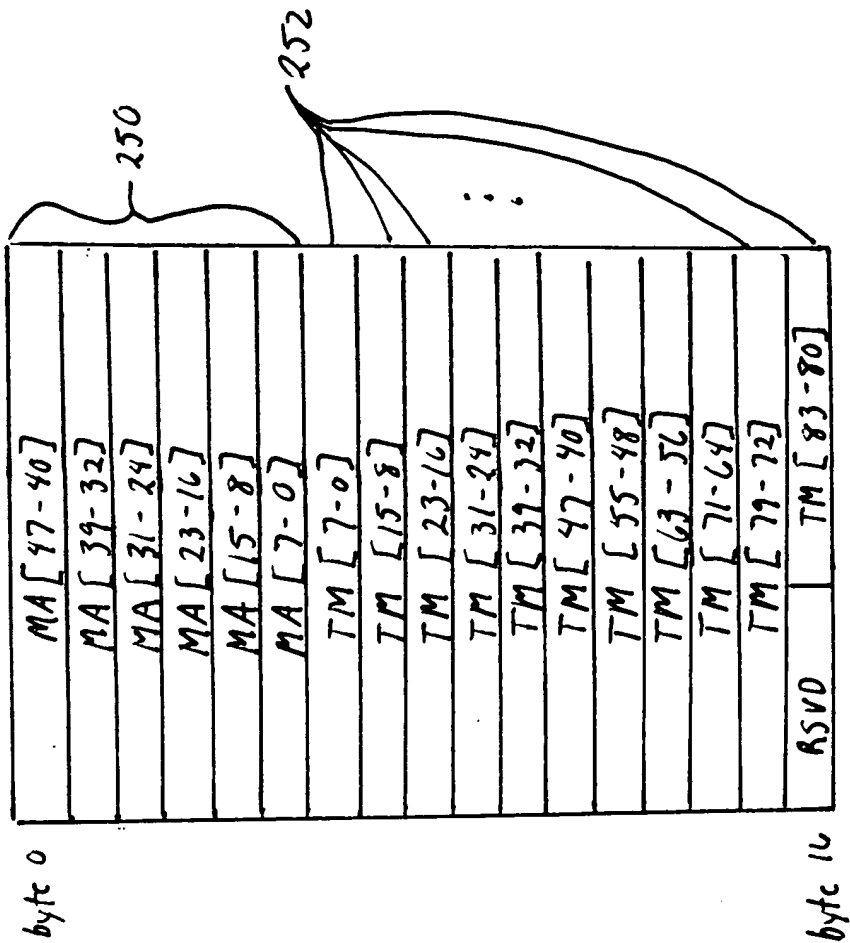


FIG. 14

004080" 4982E960

210F

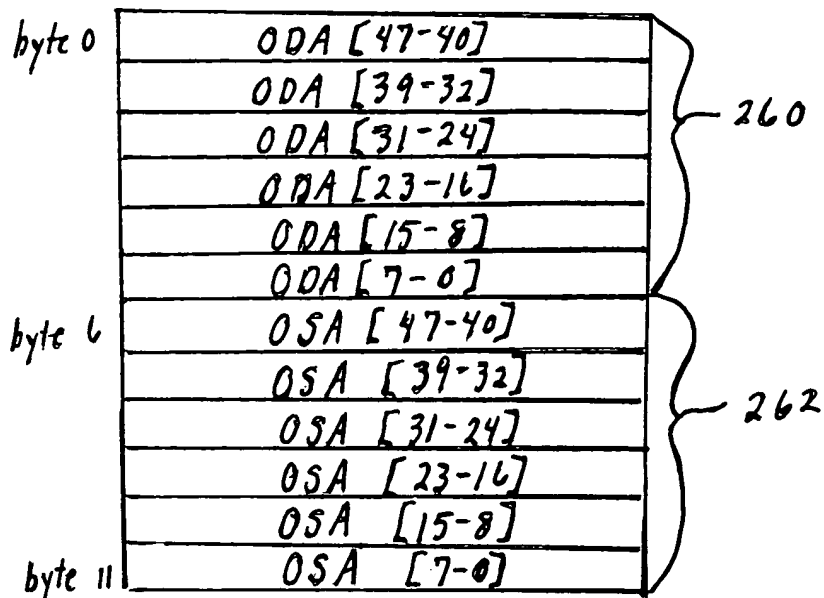


FIG. 15

Hand-drawn diagram of a data structure layout:

- A large rectangle is divided into two horizontal sections.
- The top section is labeled **EKS**.
- The bottom section is labeled **NEK**.
- Below the **EKS** section, the text **byte 0** is written.
- On the left side, a bracket indicates a size of **266** for the top section.
- On the left side, a bracket indicates a size of **268** for the bottom section.
- Below the **NEK** section, there are several lines of faint, illegible text.
- At the top right, there is a handwritten **2106** with an arrow pointing towards the diagram.
- Below the **2106**, the text **1 Octet** is written with arrows pointing left and right.

FIG. 16

210H.

1 Octet

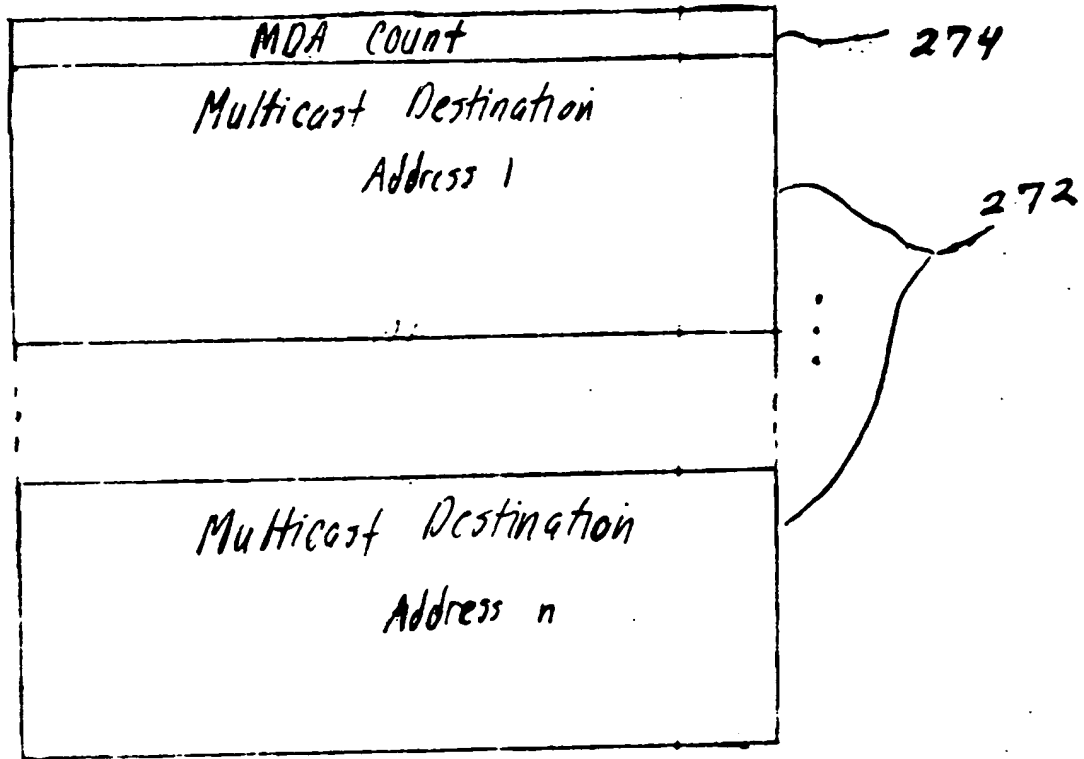
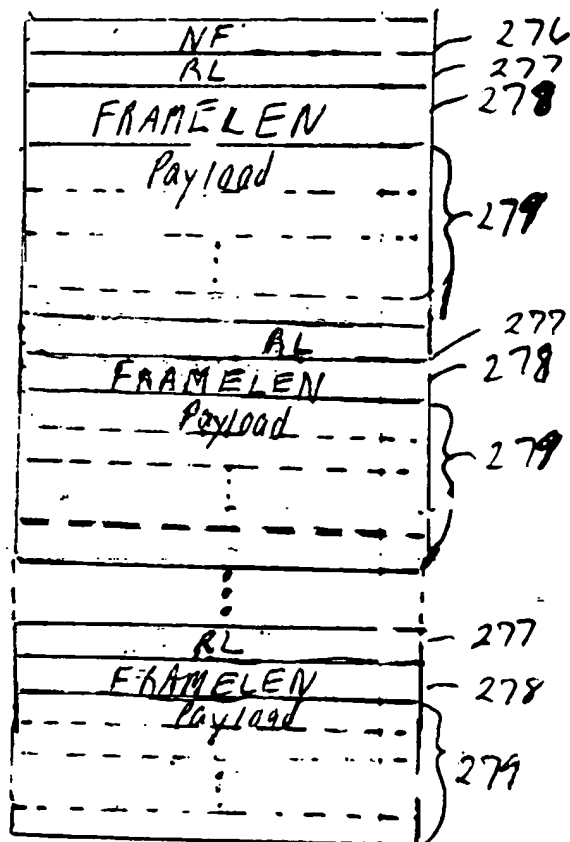


FIG. 17

09632867.080400

210I.



00672847-030403

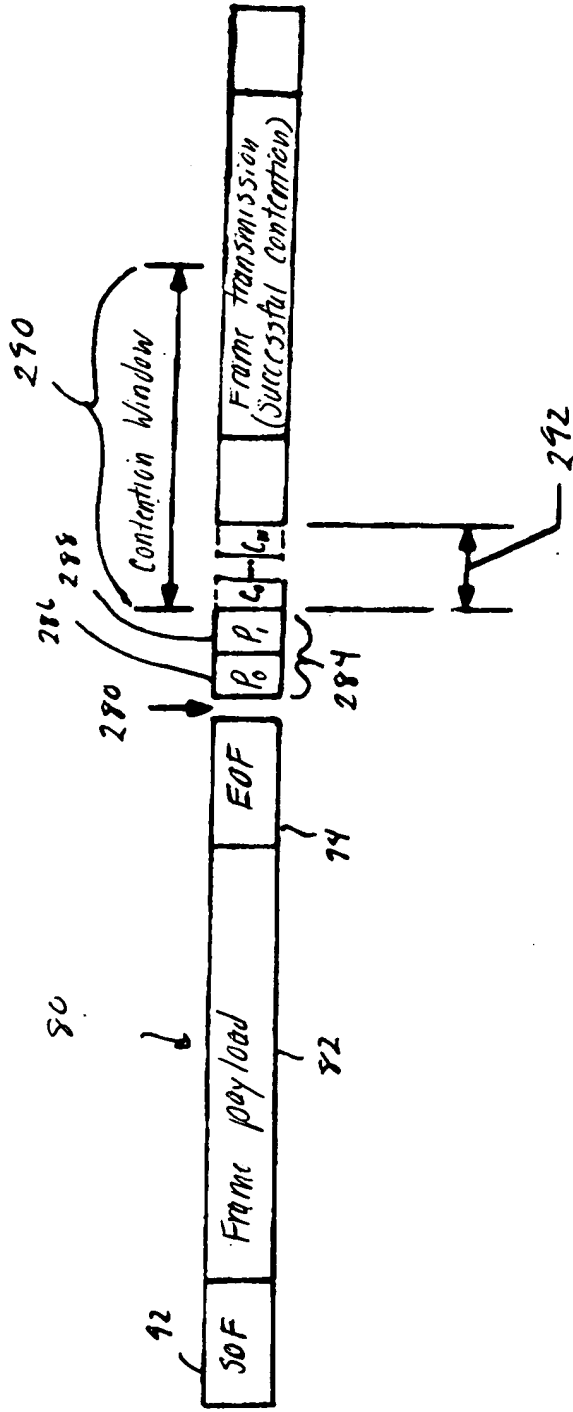


FIG. 19A

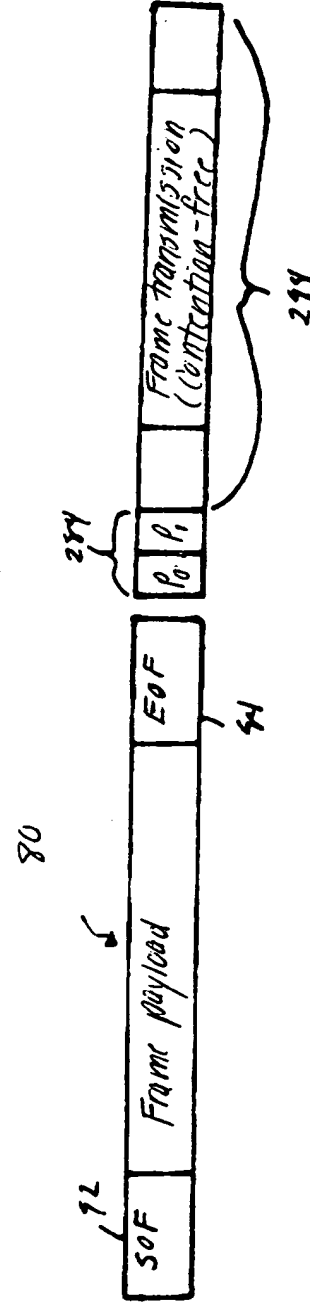


FIG. 19B

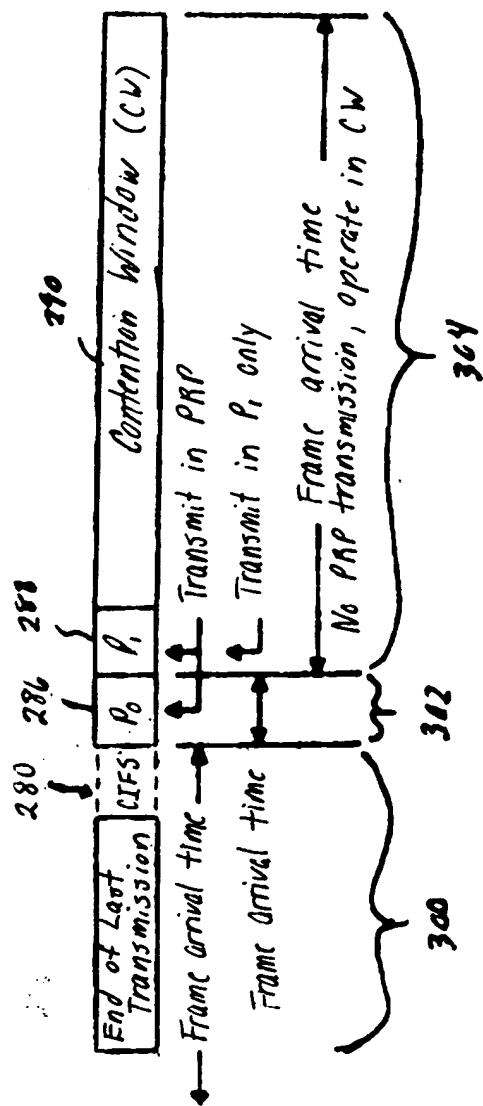


FIG. 20

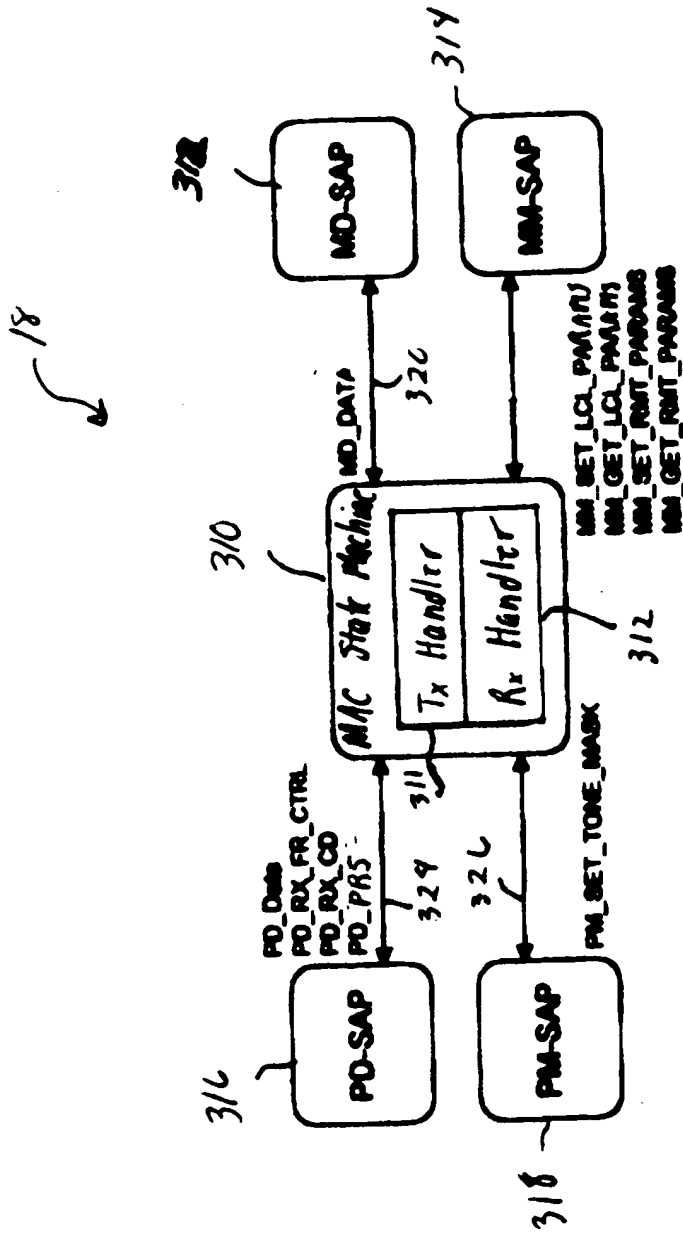


FIG. 21

004030" 2982E960

311

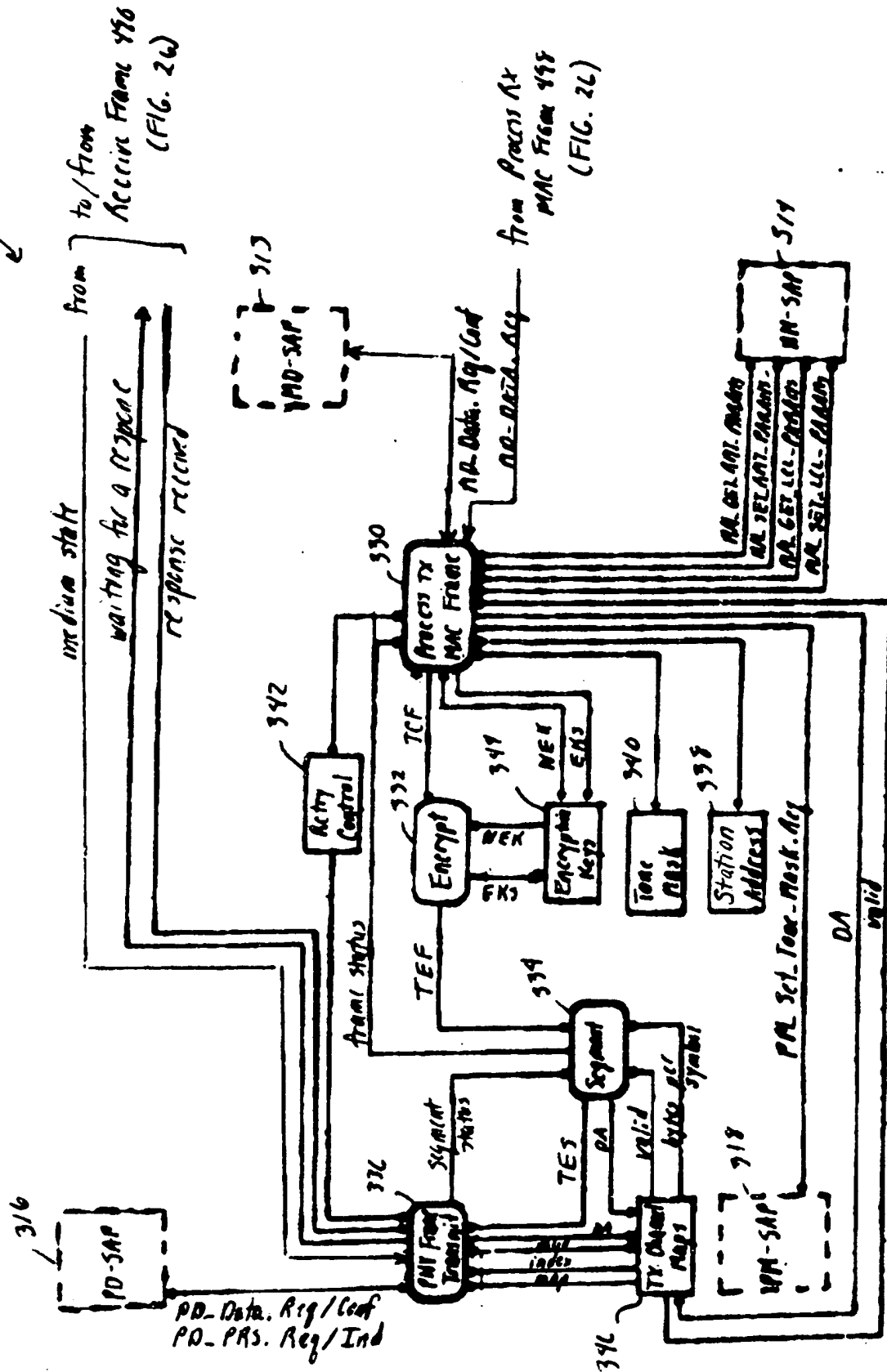


FIG. 22

09632867.080400

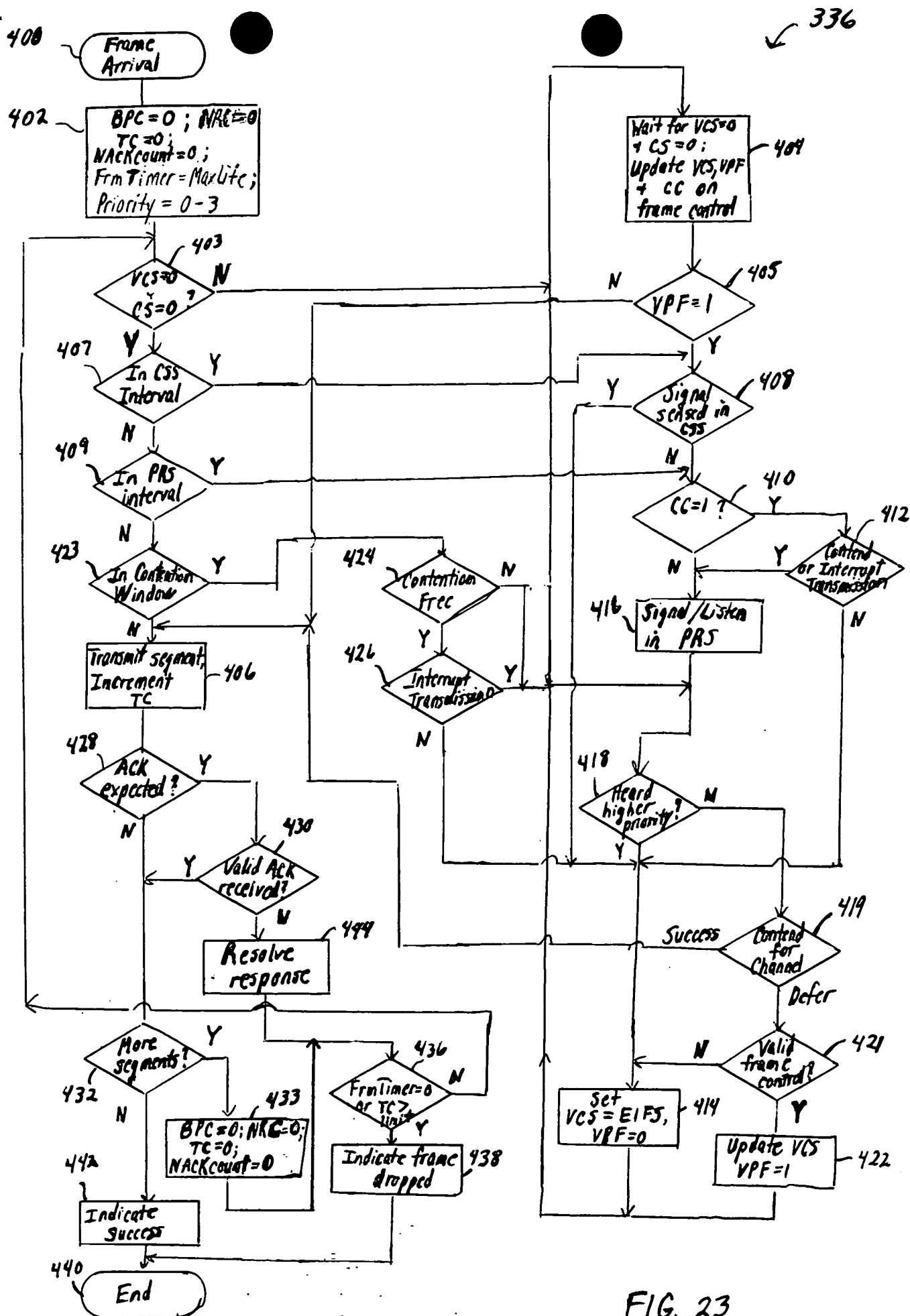


FIG. 23

004807982960

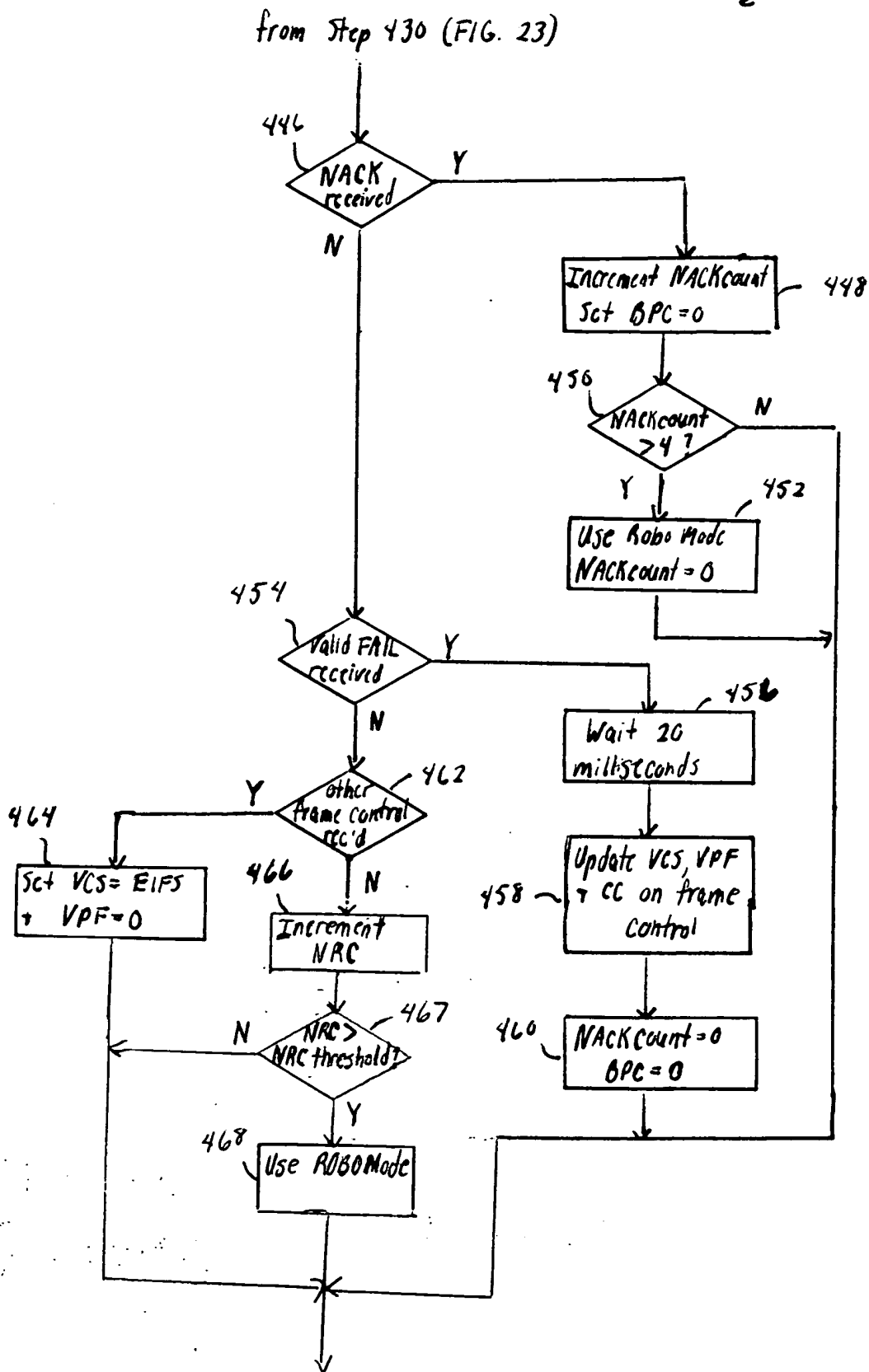


FIG. 24

From Step 415 (FIG. 23)

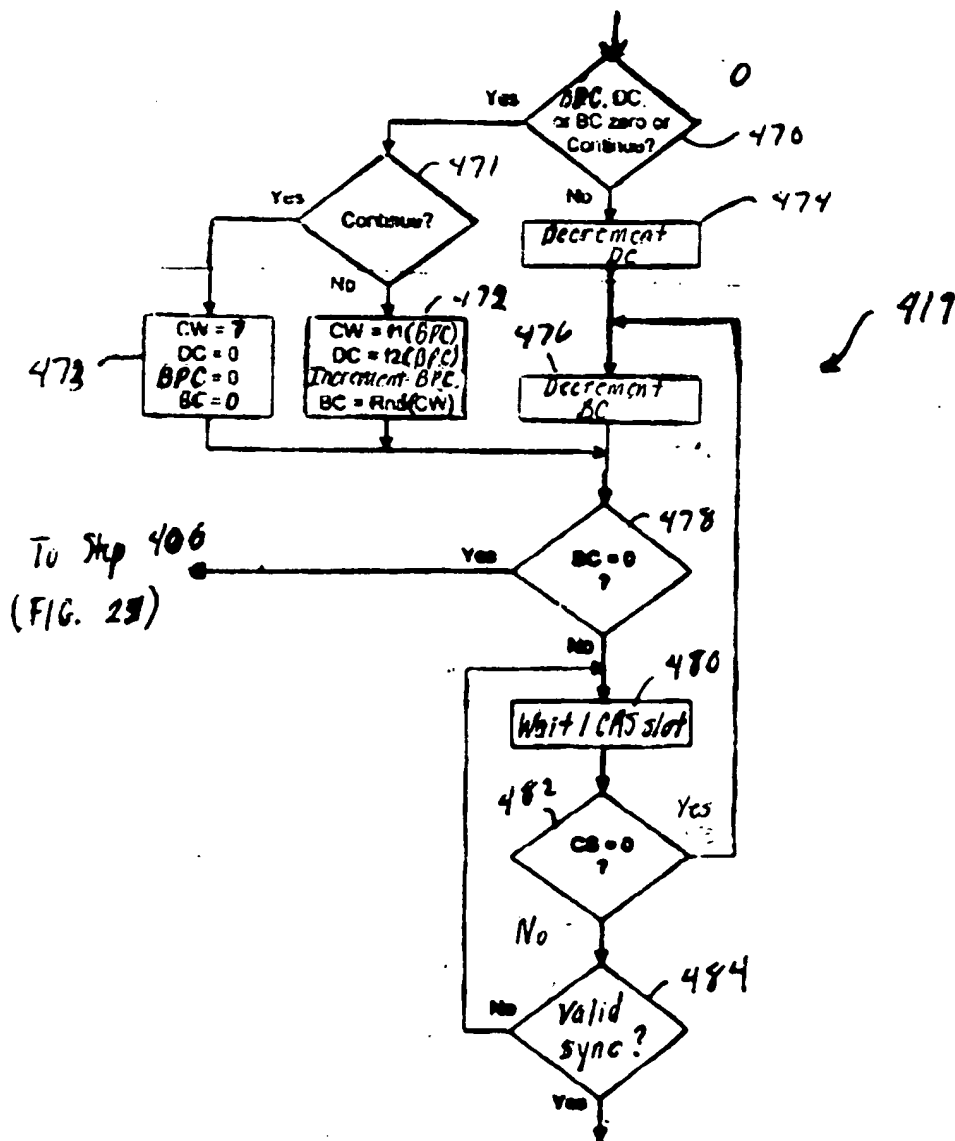


FIG. 25

006322867-080400

004080" 2982E960

312

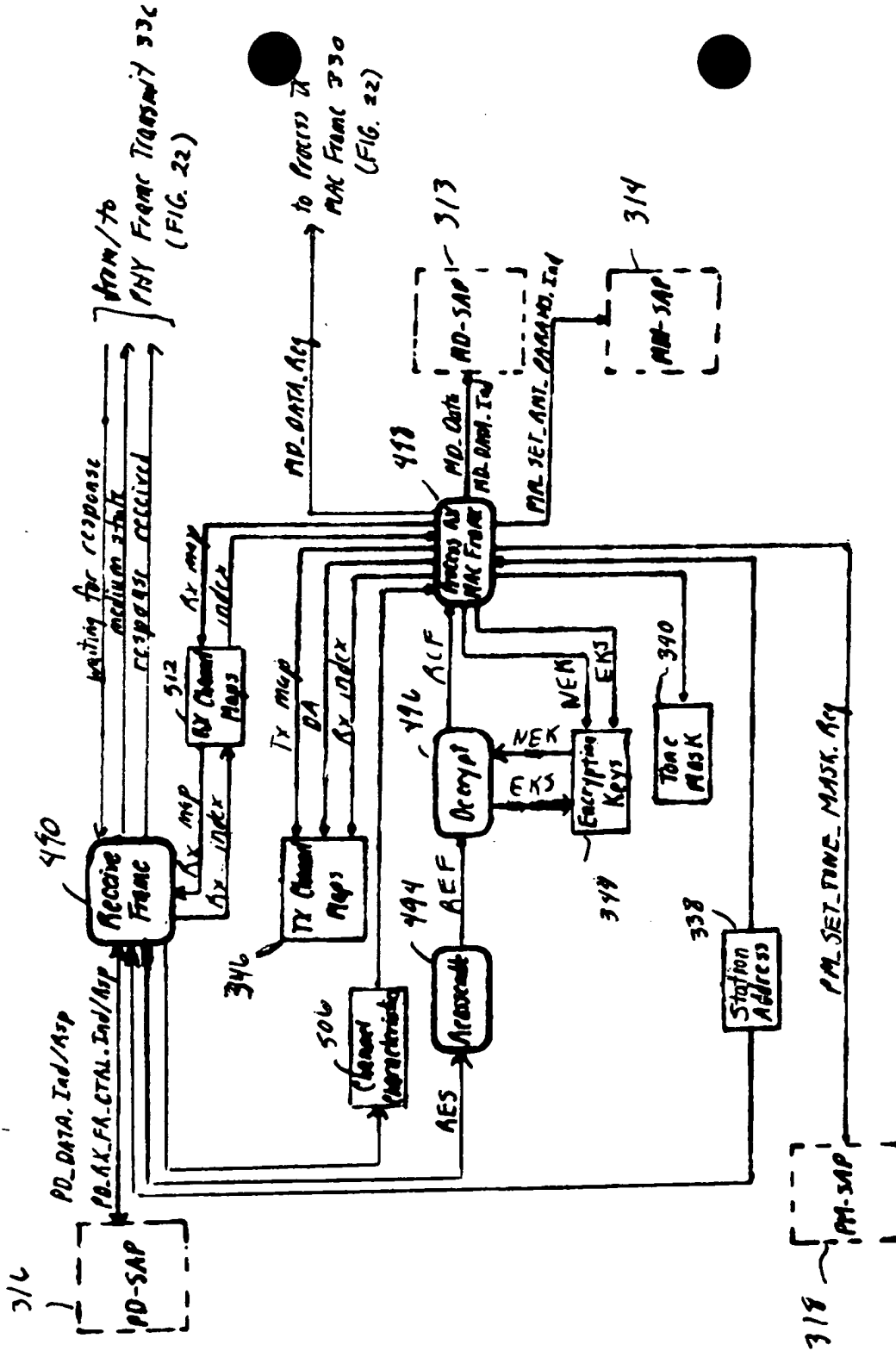
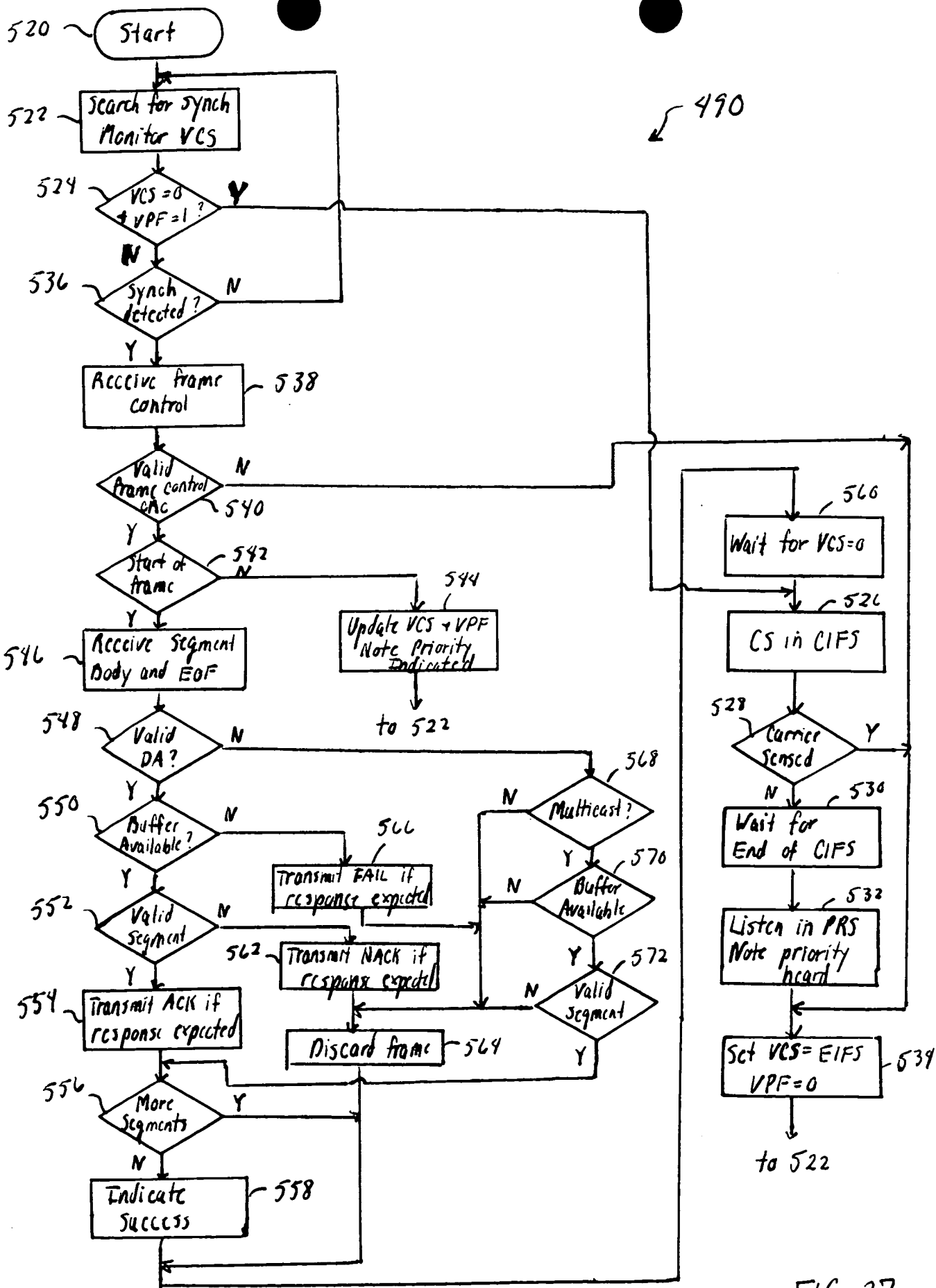


FIG. 26

00632867 080400



575

004080" 2982960

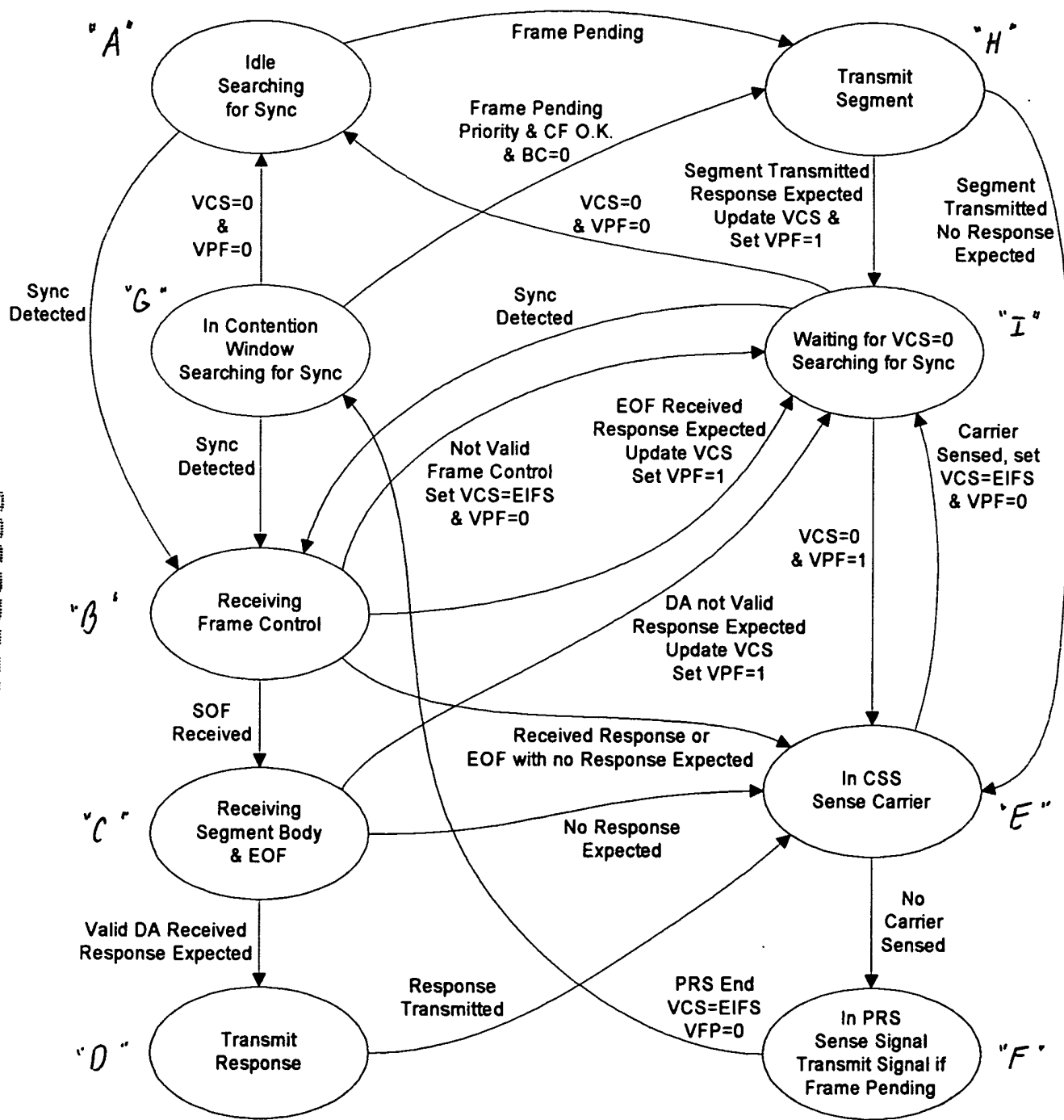


FIG. 28

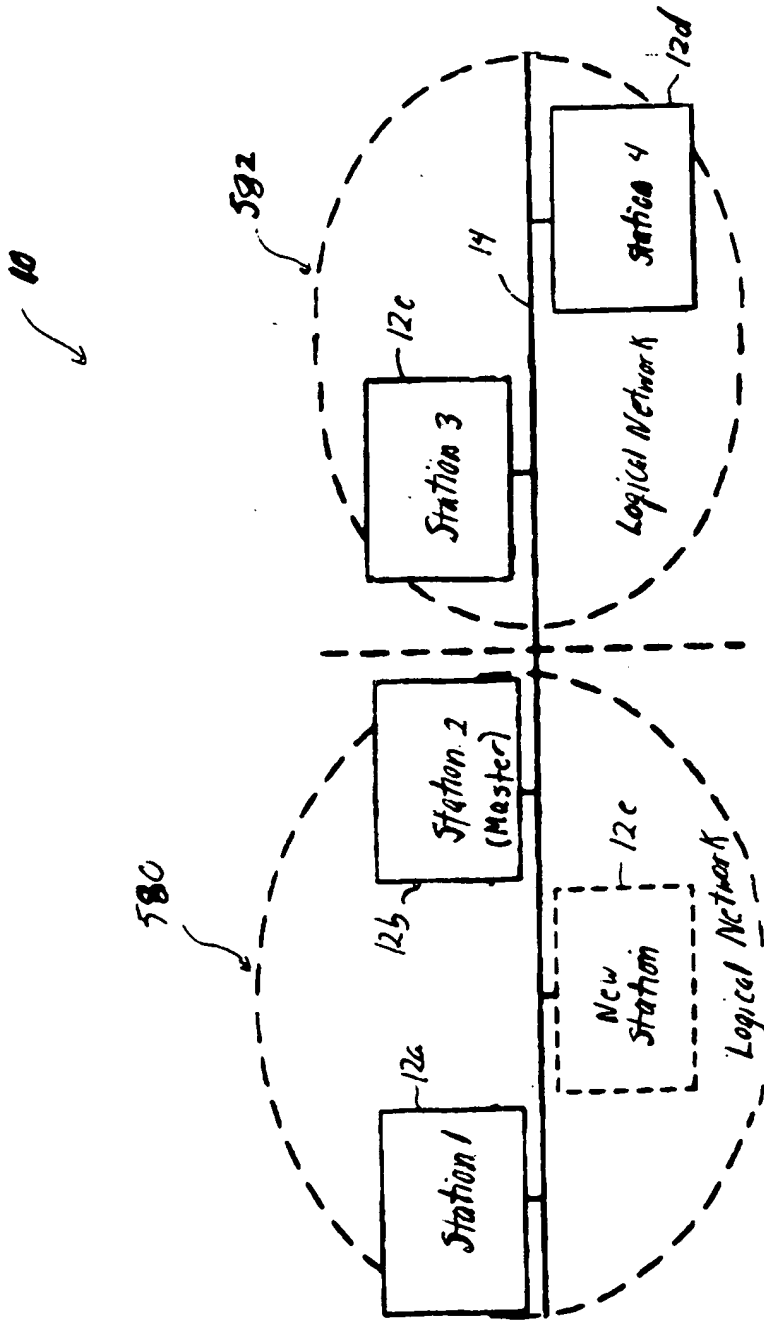


FIG. 29

004080 2982E960

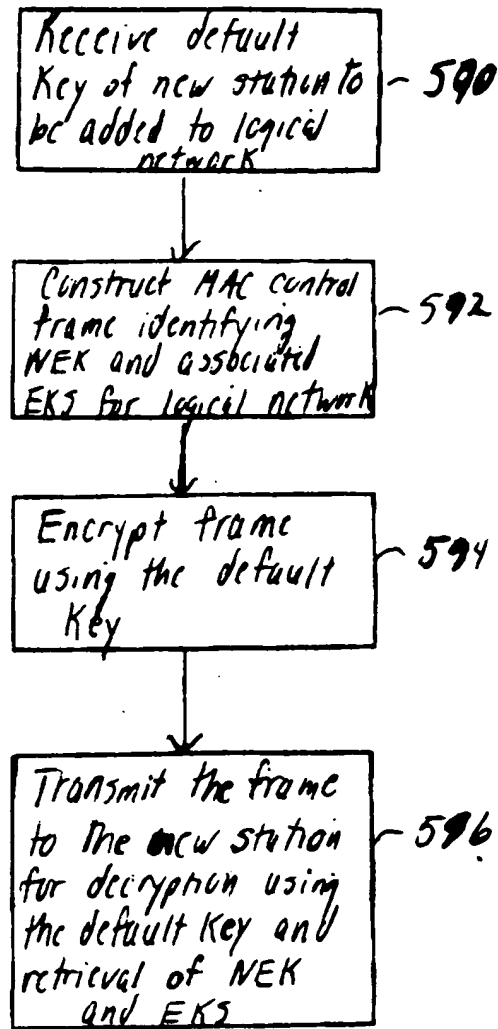


FIG.

580

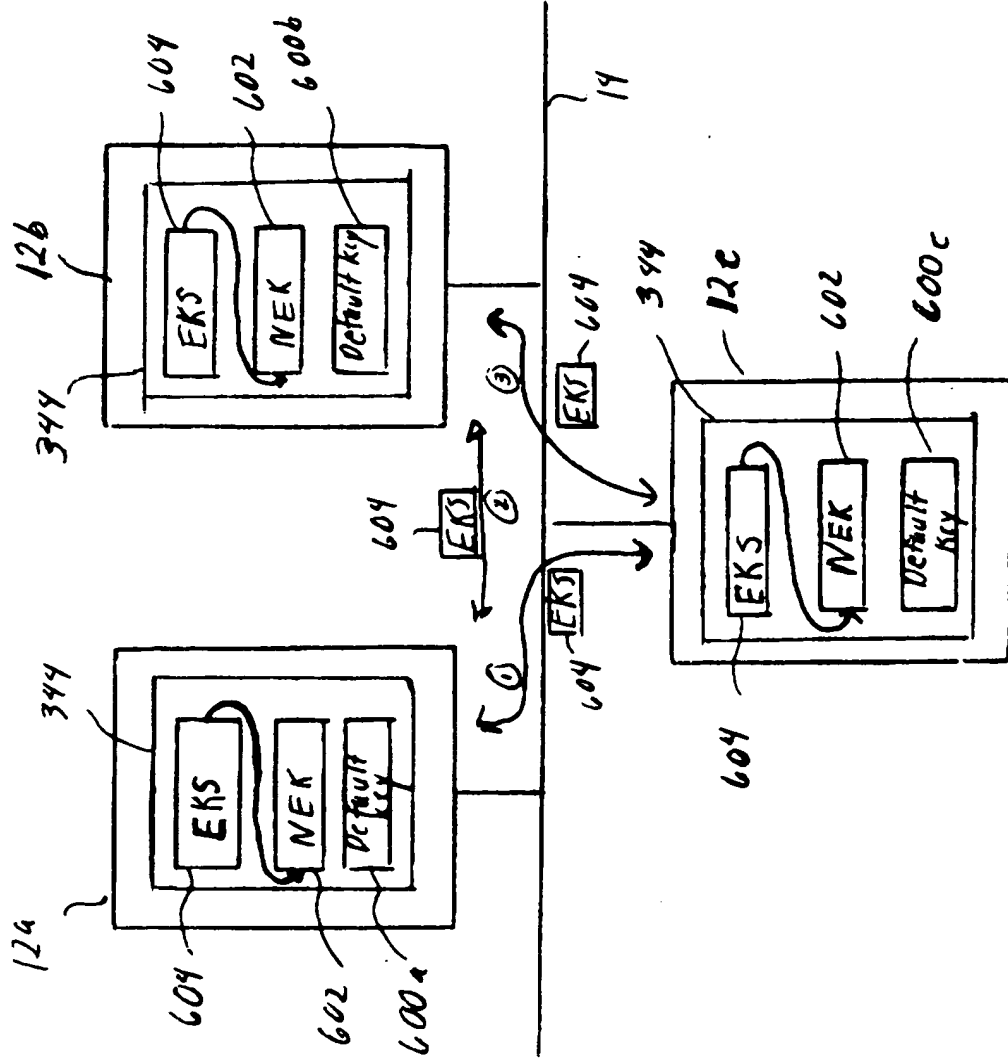


FIG. 31

620

628

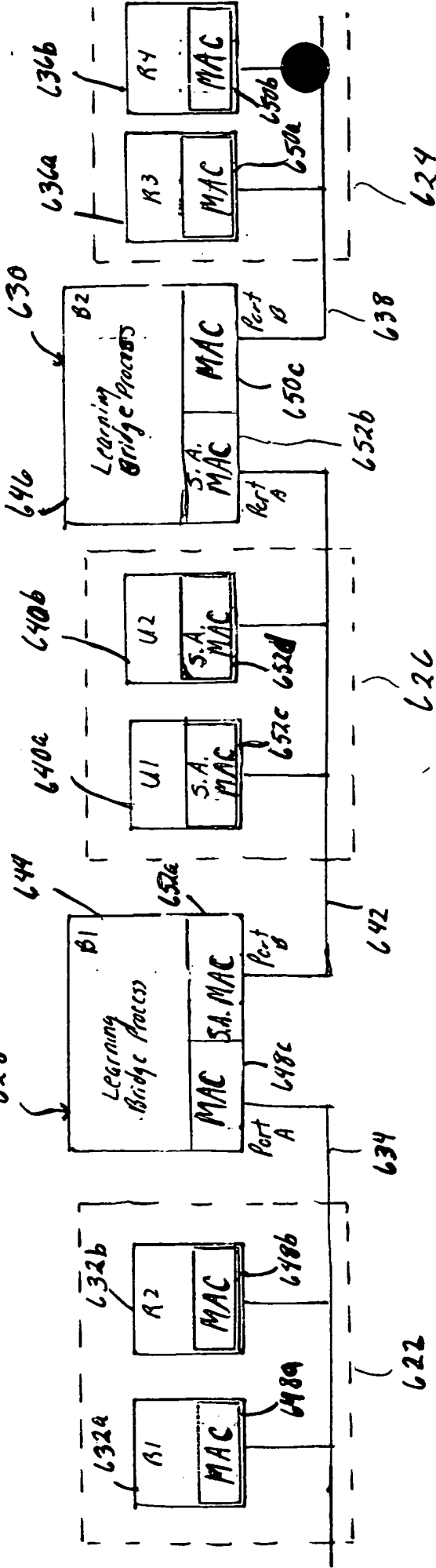


FIG. 32

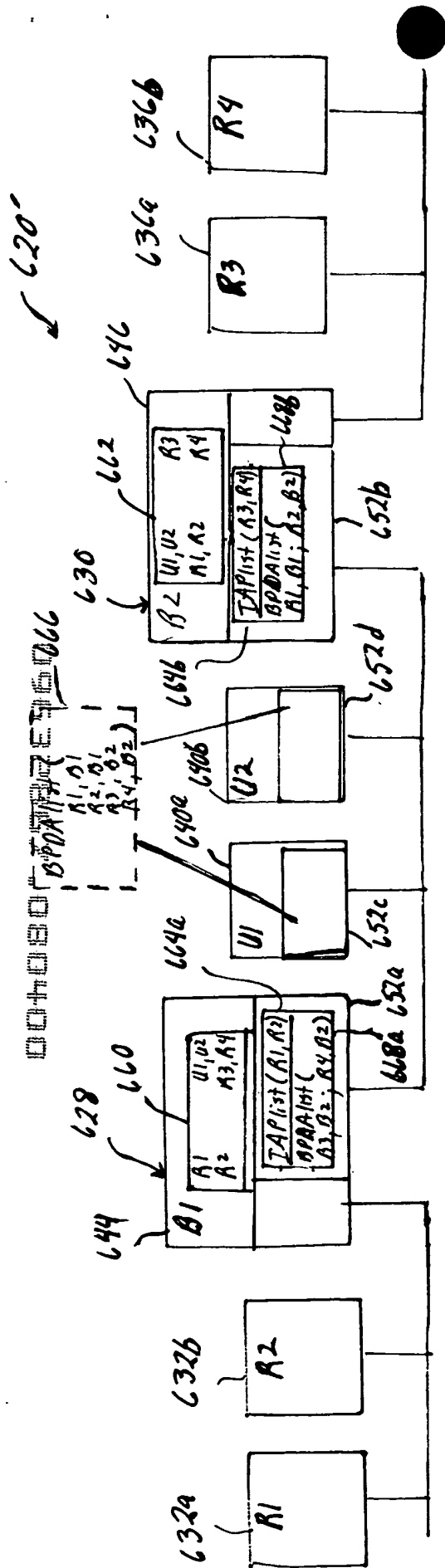


FIG. 33

09632867.080400

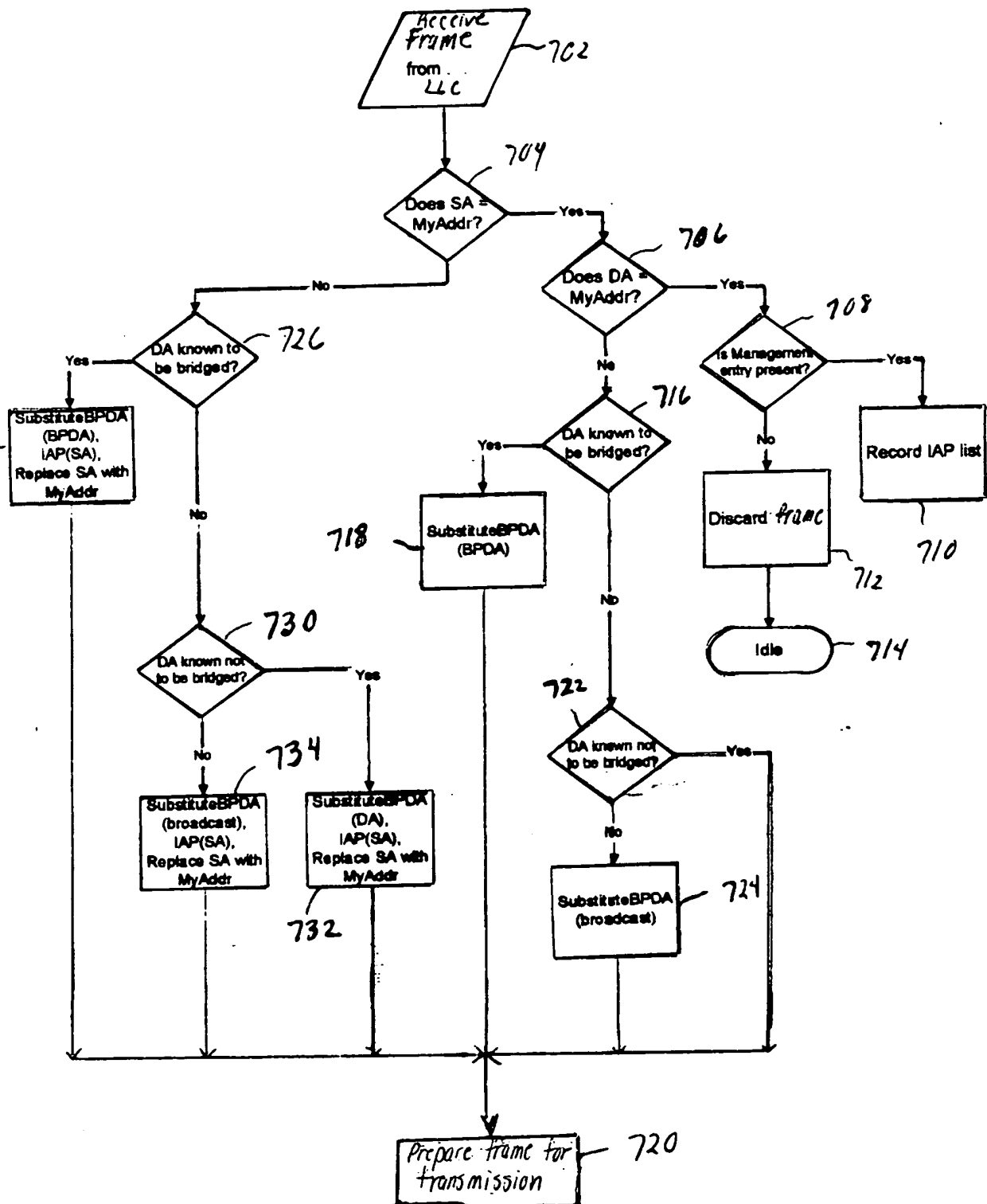


FIG. 34

004080.29825960

from FIG. 34

720

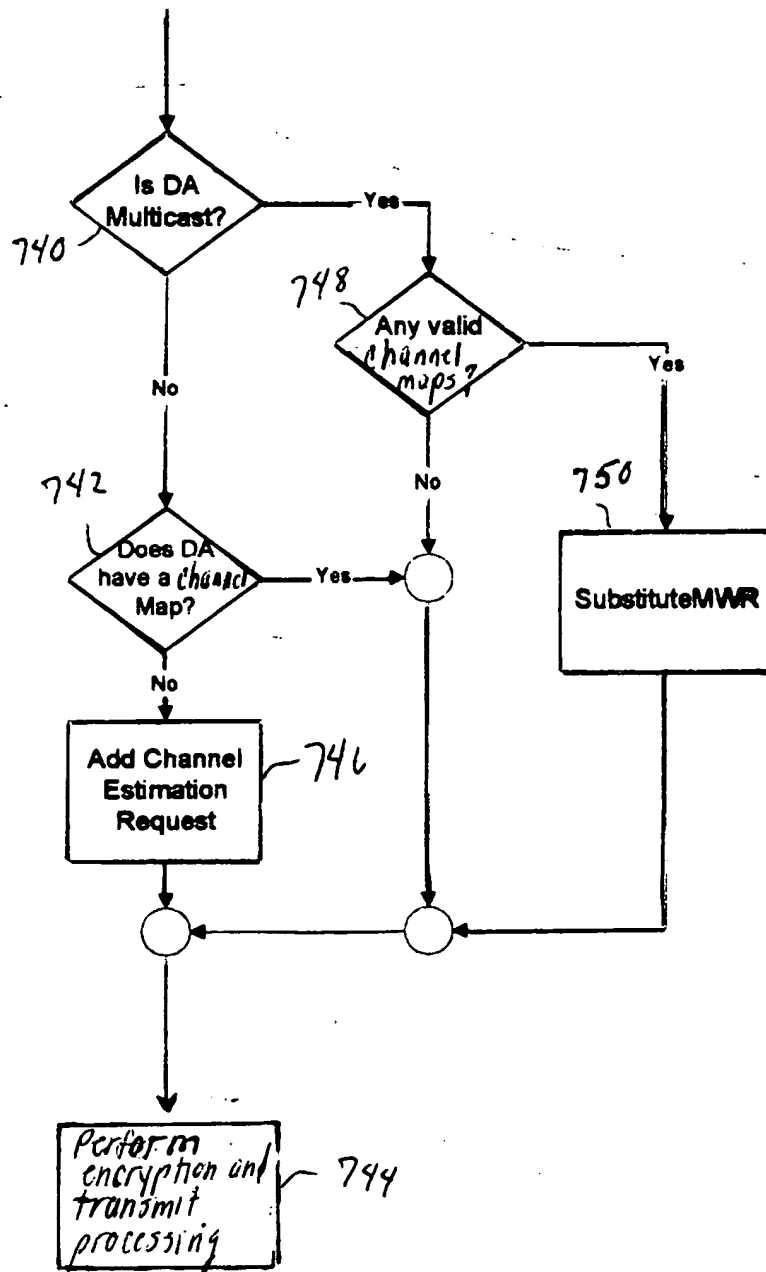


FIG. 35

004080 782E960

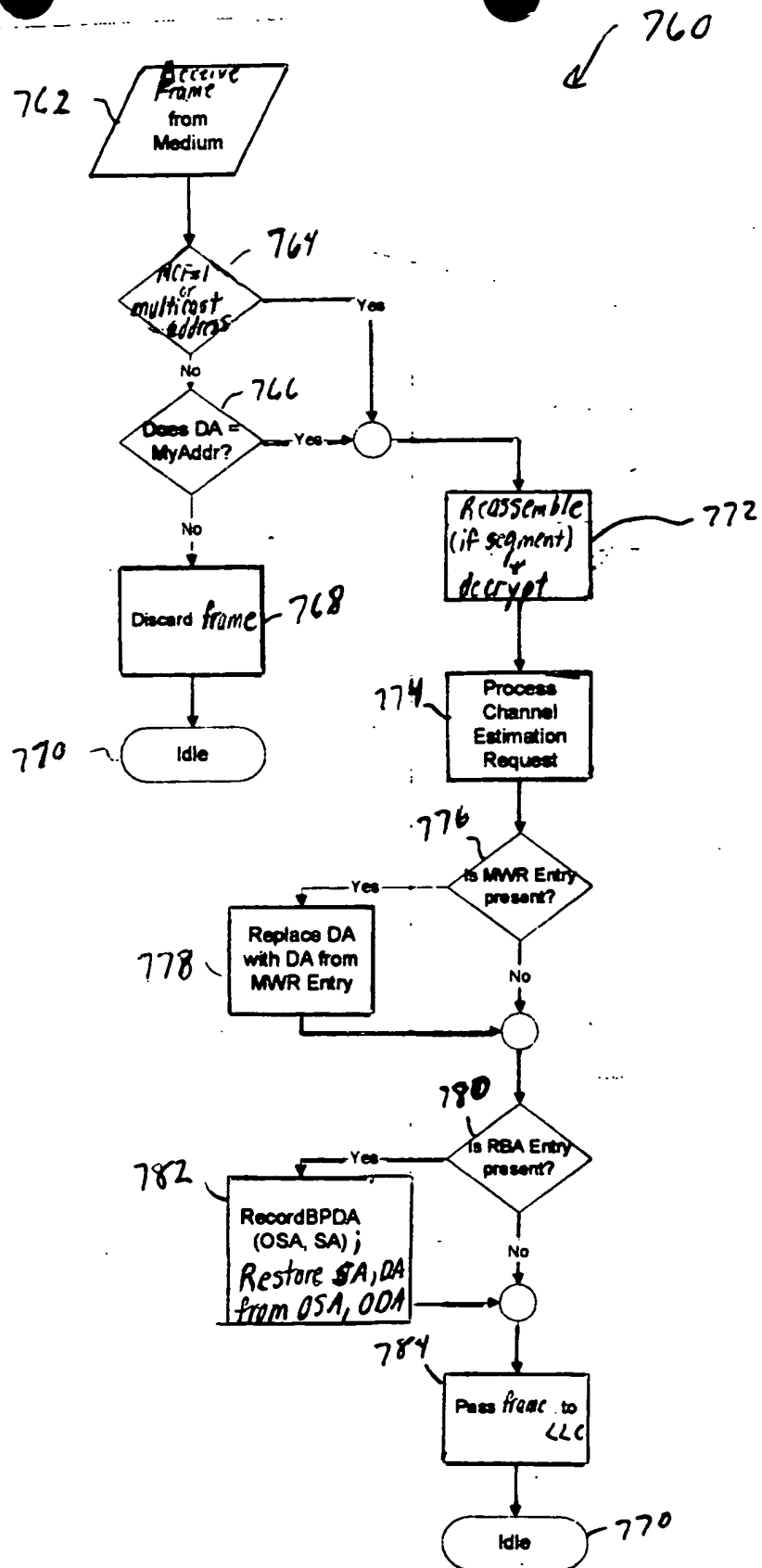


FIG. 36

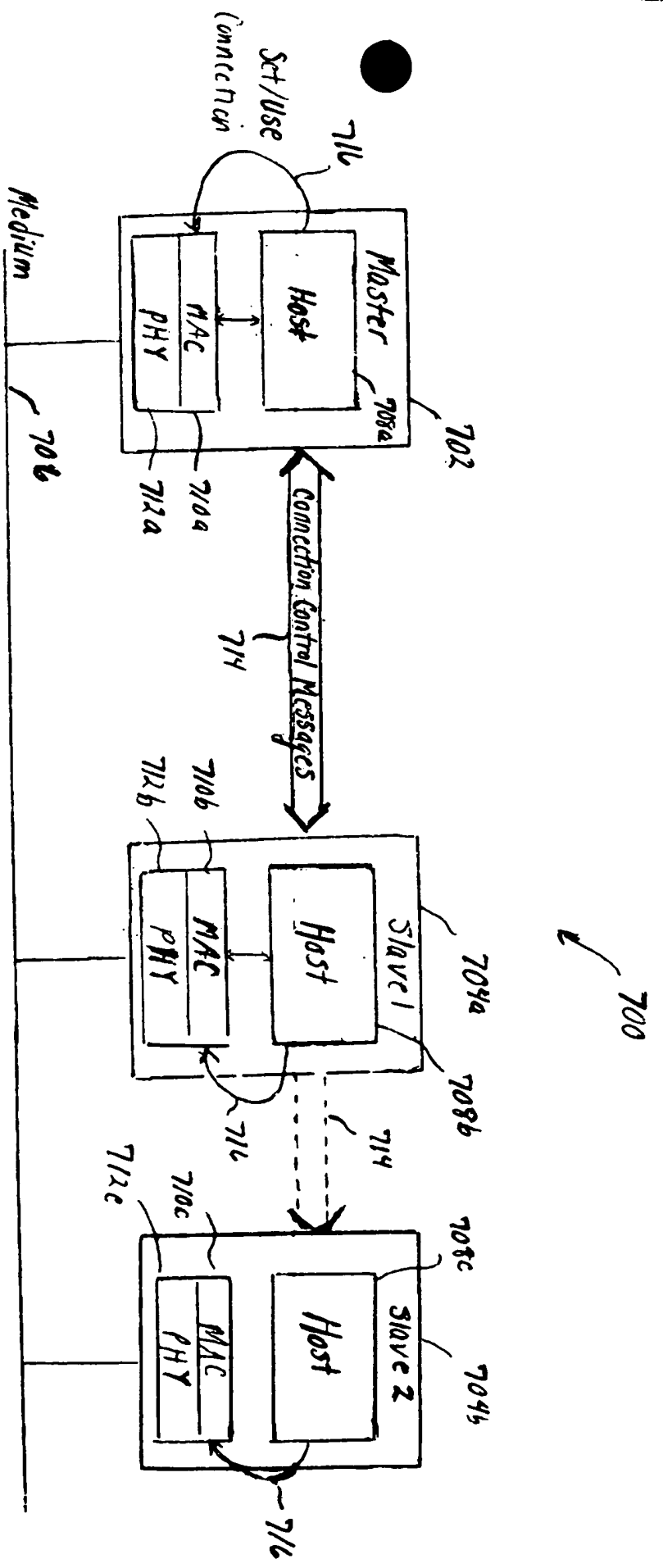


FIG. 37

740 ✓

744	746	748	750	754	756	752	760	758
Connection Number	Master	SA	SA Frame Size	Min Frame Time	Max Frame Time	Tx Frame Size	Frame Life	Control

FIG. 39A

742 ✓

Connection Number	762
-------------------	-----

FIG. 39B

800

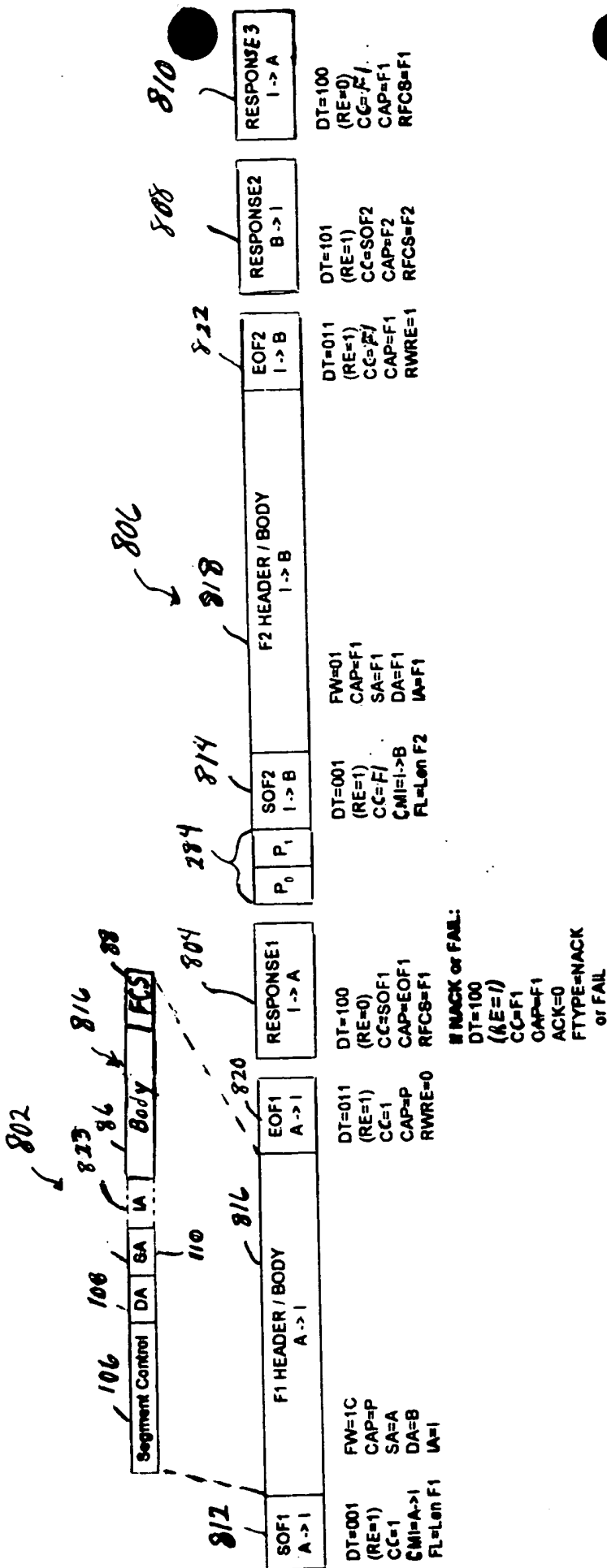


FIG. 90

824

802

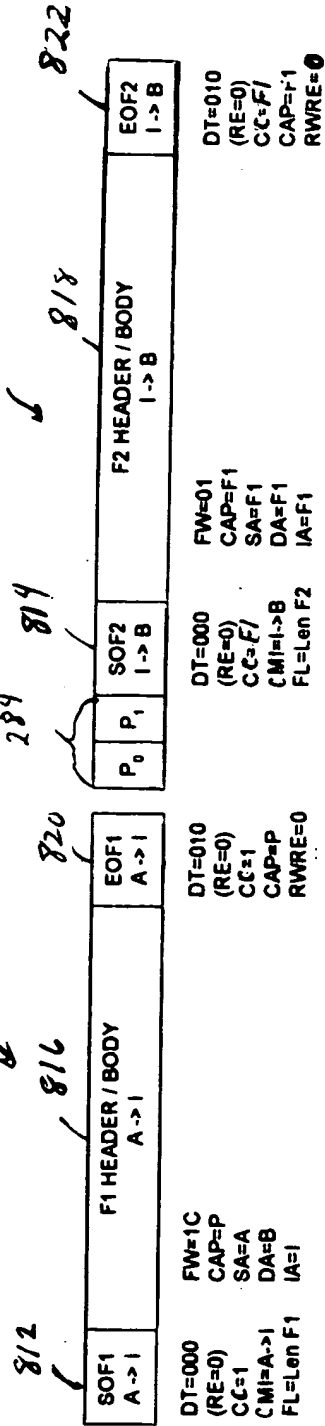


FIG. 41

836 ✓

802 ✓

806 ✓

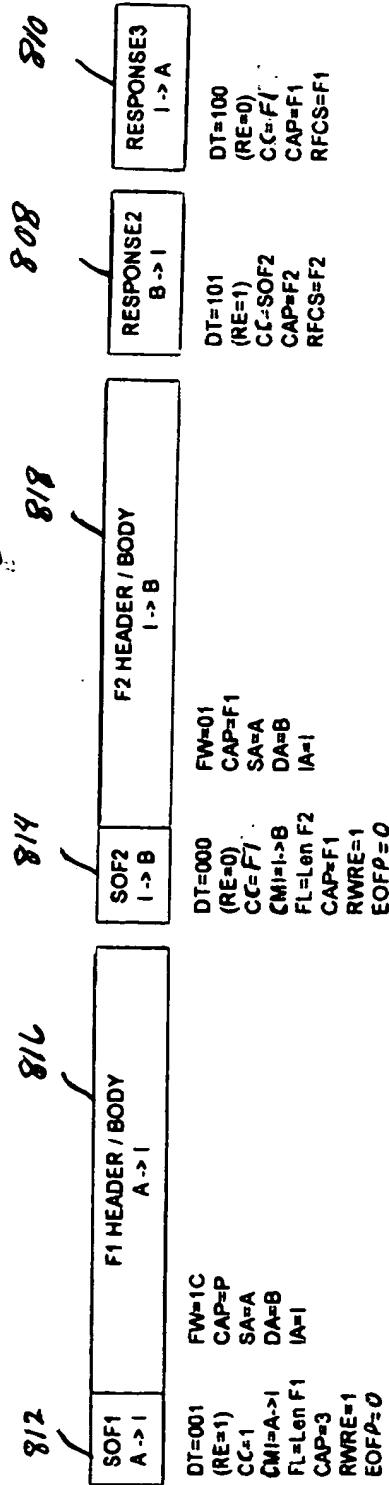


FIG. 43

004080" 49822960

838

812

816

804



DT=001
(RE=1)
CC=1
CMI=A->I
FL=Len F1
CAP=3
RWRE=1
EOFP=0

FW=1C
CAP=P
SA=A
DA=B
IA=I

DT=100
(RE=0)
CC=F1
CAP=F1
ACK=0
FTYPE=NACK
or FAIL

FIG. 44

840

812

816

814

818



DT=001
(RE=1)
CC=1
CMI=A->I
FL=Len F1
CAP=3
RWRE=0
EOFP=0

FW=1C
CAP=P
SA=A
DA=B
IA=I

DT=000
(RE=0)
CC=F1
CMI=I->B
FL=Len F2
CAP=F1
RWRE=0
EOFP=0

FW=01
CAP=F1
SA=A
DA=B
IA=I

FIG. 45

102

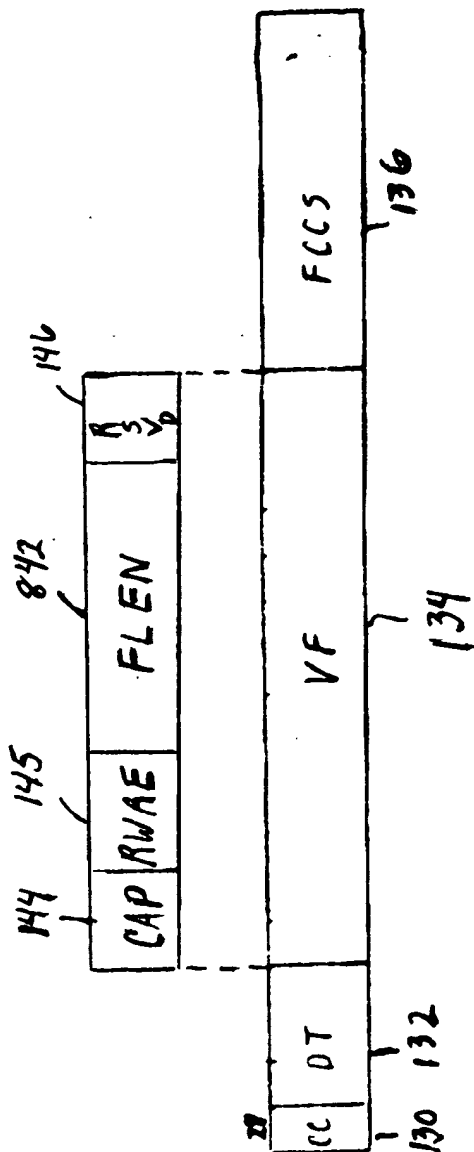


FIG. 46